Verification of the Requirements Specification

Istvan Majzik
majzik@mit.bme.hu

Budapest University of Technology and Economics
Dept. of Measurement and Information Systems

Overview

- Inputs and outputs of the phase
- Preparing the requirements specification
  - Formal languages
  - Semi-formal and structured methods
  - Example: SysML
- Verification tasks
  - General aspects and verification techniques
  - Verifying completeness and consistency
- Managing requirements
  - Traceability
  - Basic tasks and tool support

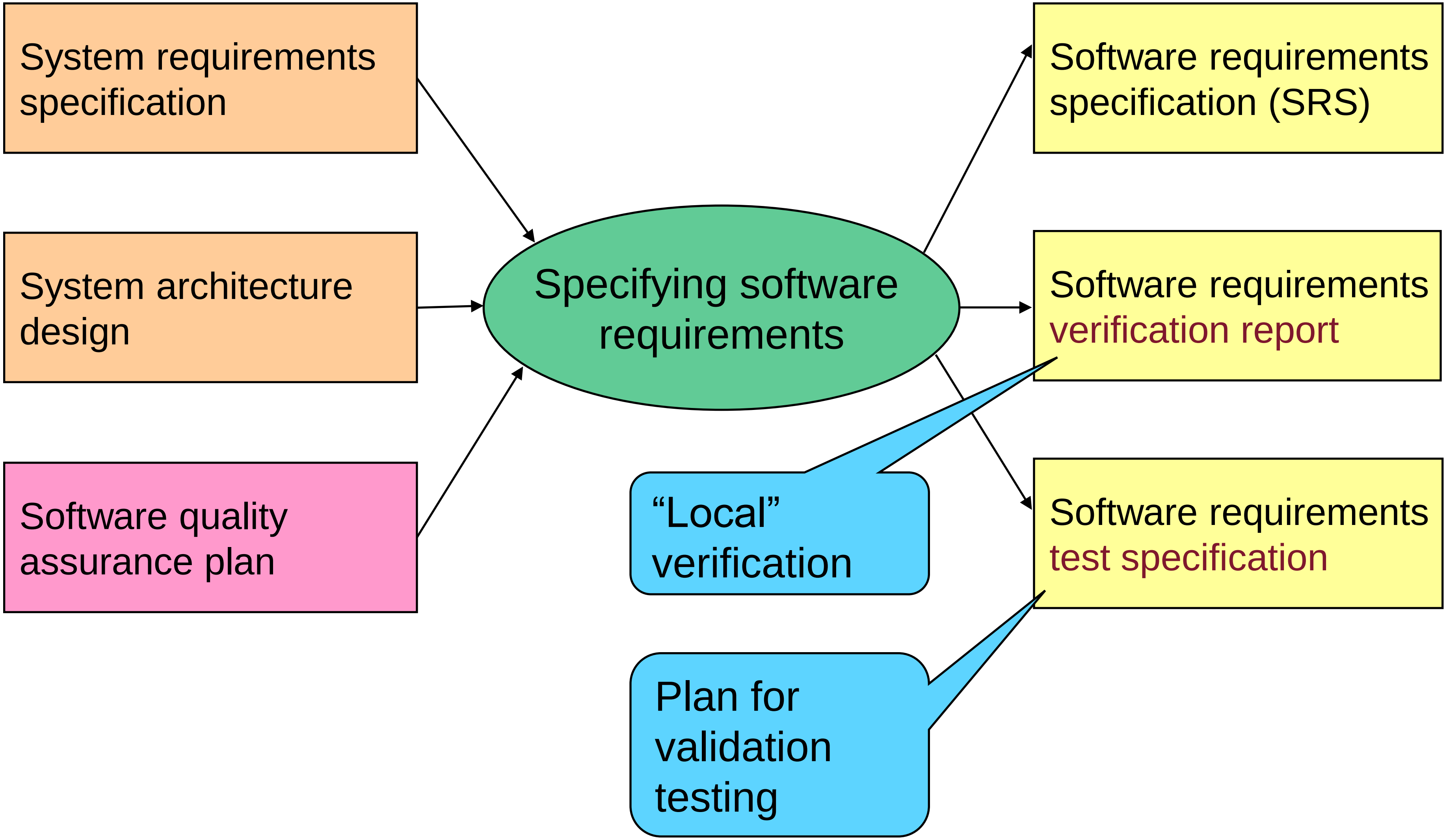
Inputs and outputs of the phase

Inputs and outputs

Related: Software Quality Assurance Plan

and Software Verification Plan

Inputs and outputs of the phase



System requirements specification

System architecture design

Software quality assurance plan

Specifying software requirements

Software requirements specification (SRS)

Software requirements verification report

Software requirements test specification

"Local" verification

Plan for validation testing

Software Quality Assurance Plan

- Goals:
  - Preventing systematic faults and controlling residual faults
  - Determining the required technical and control activities
- Main aspects to be included:
  - Activities, their input and output criteria in the lifecycle
  - Quantitative quality expectations (e.g., ISO/IEC 9126)
  - Specification of it own review and maintenance
- Methods for checking external suppliers
  - Compliance of the QA Plan of the supplier
  - Verification of external software components
- Issue tracking
  - Documentation and feedback mechanisms
  - Analysis of issues (root causes)
  - Diagnosis and maintenance/repair activities and techniques
  - Verification and validation of corrections
  - Fault avoidance

Software Verification Plan

- Often a separate plan (especially in safety-critical systems)
- Planning the verification activities
  - Planning the techniques and measures (from the development standard)
  - Determining acceptance criteria
- Overall aspects of verification:
  - “Local” checking of the given development step: Completeness, consistency
  - Conformance checking: W.r.t. the output of previous phases
- Details:
  - Participants roles and responsibilities
  - Tools (e.g., test equipment)
  - Evaluation of verification results (acceptance criteria)
    - Checking the required test coverage
    - Evaluation of quality requirements

Software requirements specification - Terminology

- Requirement
  - Incoming need, vision, expectation
    - From the future users
    - From stakeholders (management, operator, authority, ...)
  - Basis for validation
- Requirements specification
  - Requirements in converted form, for the designers
    - Result of requirement analysis
    - Abstraction, structuring, filtering applied
  - Several types of requirements
    - Property specification, behavior specification, …
    - Later: architecture specification (/design), module specification, …
  - Basis for verification

Preparing the requirements specification

Formal languages

Semi-formal and structured methods

Example: SysML

Approaches for specifying requirements

- Contents of the requirements specification
  - Functional requirements
  - Extra-functional requirements
- Natural language based specifications
  - Problems with unambiguity, verifiability
- Possible solutions:
  - Using strict specification language (e.g., formal, or semi-formal)
  - Using verified “specification patterns” (e.g., for safe behavior)
  - Systematic verification after the requirement specification phase
- Example: Solutions proposed by EN 50128
  - Formal methods (VDM, Z, B, TL, PN, ...)
  - Semi-formal methods (diagram based techniques, SysML)
  - Structured methods (JSD, SADT, SSADM, …)
  - Natural language based description (explanation) is mandatory

Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic languages (ADT, OBJ, …)
- Process description languages (CSP, CCS, …)
- Logic languages (HOL, CTL*, …)
- Constructive languages (NUPRL, …)
- Hybrid or wide spectrum languages (CPN, E-LOTOS, …)

Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic langua es
- Process desc on lan
- Logic lan
- , …)
- LOTOS, …)

Mathematical model:

- Elements in the system (set-theoretic structures like sets, subsets, relations)
- Functions, operations, events (with pre- and post-conditions, invariants)

Example: Specification of an access control system (in Event-B):

Persons:	`prs ≠ 0, p ∈ prs`	(set)
Buildings:	`bld ≠ 0, b ∈ bld`	(set)
Authorization:	`aut ∈ prs ↔ bld`	(binary relation)
Situation:	`sit ∈ prs → bld`	(complete function)
Invariant:	`sit ⊆ aut`	

An event (change of situation):

```
  pass = ANY p,b WHERE (p,b)∈aut ∧ sit(p)≠b
      THEN  sit(p):=b END
```

Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic languages (ADT, OBJ, …)

anguages

(CPN, E-LOTOS, …)

Abstract data types: sorts (set of values), operations, properties as equiations

```
Type Boolean is
    sorts Bool
    opns
        false, true :       -> Bool
        not : Bool          -> Bool
        and : Bool, Bool    -> Bool
    eqns
        forall x, y: Bool
        ofsort Bool
            not(true) = false;
            not(false) = true;
            x and true = x;
```

Abstract algebra and category theory

- Abstract data types: values, operations, properties
- First order logic is typical

Overview of the types of formal languages

- Model-oriented langua
- Algebraic languages
- Process description languages (CSP, CCS, …)
- Logic l
- Cons
- Hybr

- Processes: Sequential execution of statements
- Operations among the processes (synchronization, communication)

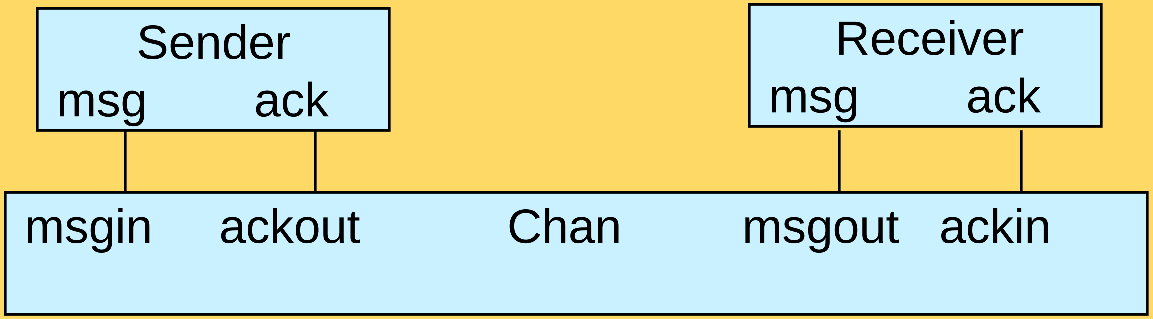
Example: Process algebra language (CCS):

Sender = $\overline{\mathtt{msg}}$.ack.**Sender**

Receiver = msg.$\overline{\mathtt{ack}}$.**Receiver**

Chan = msgin.$\overline{\mathtt{msgout}}$.**Chan** + ackin.$\overline{\mathtt{ackout}}$.**Chan**

Proc = **Sender**[msgin/msg,ackout/ack] | **Chan** | **Receiver**[msgout/msg, ackin/ack]



Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic languages (ADT, OBJ, …)
- Process description languages (CSP, CCS, …)
- Logic languages (HOL, CTL*, …)
- Constructive lang
- Hybrid or wide spec

.)

- Formal mathematical logic (first order or higher order logic)
- Temporal logics (with temporal operators like “future”, “next time”, “until”, “before”)

Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic languages
- Process description l
- Logic languages
- Constructive languages (NUPRL, …)
- Hybri

Constructive logic systems (computable functions): Proof of a property of a function at the same time provides a construction (implementation)

Example for a non-constructive proof (in mathematics)

- The existence of an artifact with a given property can be proven without giving exactly what is that artifact
  - Example: There exist $a,b \notin Q$ such that $a^b \in Q$
- Properties with non-constructive proof are not feasible for software specification, this way restrictions are needed that guarantee the synthesis of functions

Overview of the types of formal languages

- Model-oriented languages (VDM, Z, B, …)
- Algebraic languages (ADT, OBJ, …)
- Process description languages (CSP, CCS, …)
- Logic languages (HOL, CTL*, …)
- Constructive languages (NUPRL, …)
- Hybrid or wide spectrum languages (CPN, E-LOTOS, …)

- Properties and advantages of different formalisms are combined, e.g.,
  - LOTOS: process algebra + ADT
  - CPN: Petri-nets + data manipulation (ML)

Semi-formal languages: Examples

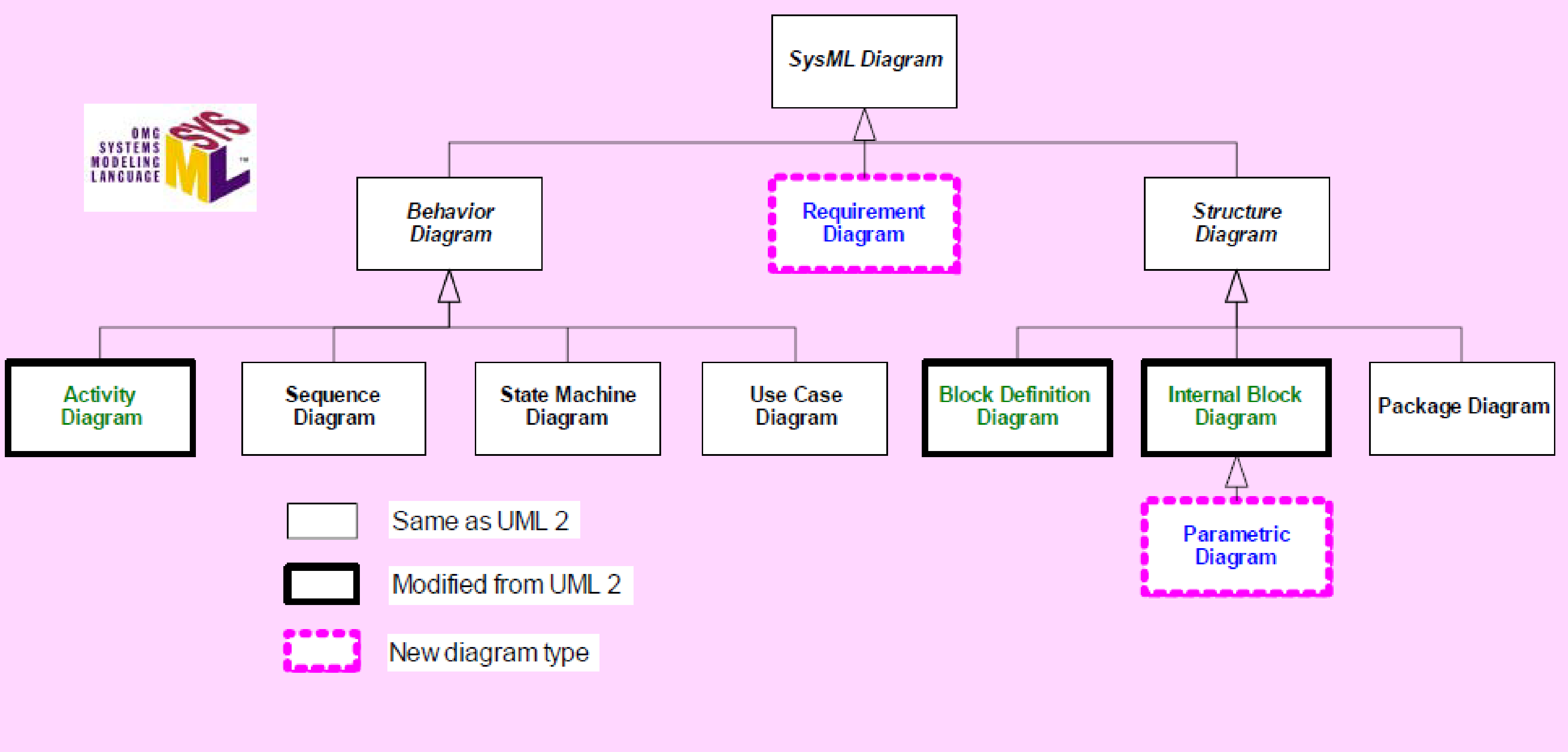
- Description of the structure:
  - (Functional) block diagrams
- Description of data flow:
  - Data flow diagrams, data flow networks
  - (Message) sequence diagrams
- Description of the control flow:
  - Control flow diagram, state machine, statechart
- Description of logic conditions:
  - Truth tables
  - Constraint languages (e.g., OCL with structure)

Structured methodologies: Historical examples

- Jackson System Development (JSD)
  - Entity structure: Entities + actions (ordering) + processes
  - Network: Communicating sequential processes
- Real-time Yourdon (Ward-Mellor)
  - Basic: Environment (input events) + behavior (response)
  - Construction: Processes (+ processors)
- SSADM
  - Data model (entity relationship diagram)
  - Data flow diagram (processes, data storage)
  - Entity diagram (life history)
  - Entity effects
- Structured Analysis and Design Technique (SADT)
  - Activity-factor diagram: tasks + relations; input, control, resource, output
- ROOM: Real-Time Object-Oriented Modeling

Semi-formal requirements specification: SysML

- Systems Modeling Language
  - UML subset and extensions for system modeling
  - Novelties: Requirement and Parametric diagram



SysML Diagram

Behavior Diagram

Requirement Diagram

Structure Diagram

Activity Diagram

Sequence Diagram

State Machine Diagram

Use Case Diagram

Block Definition Diagram

Internal Block Diagram

Package Diagram

Parametric Diagram

Same as UML 2

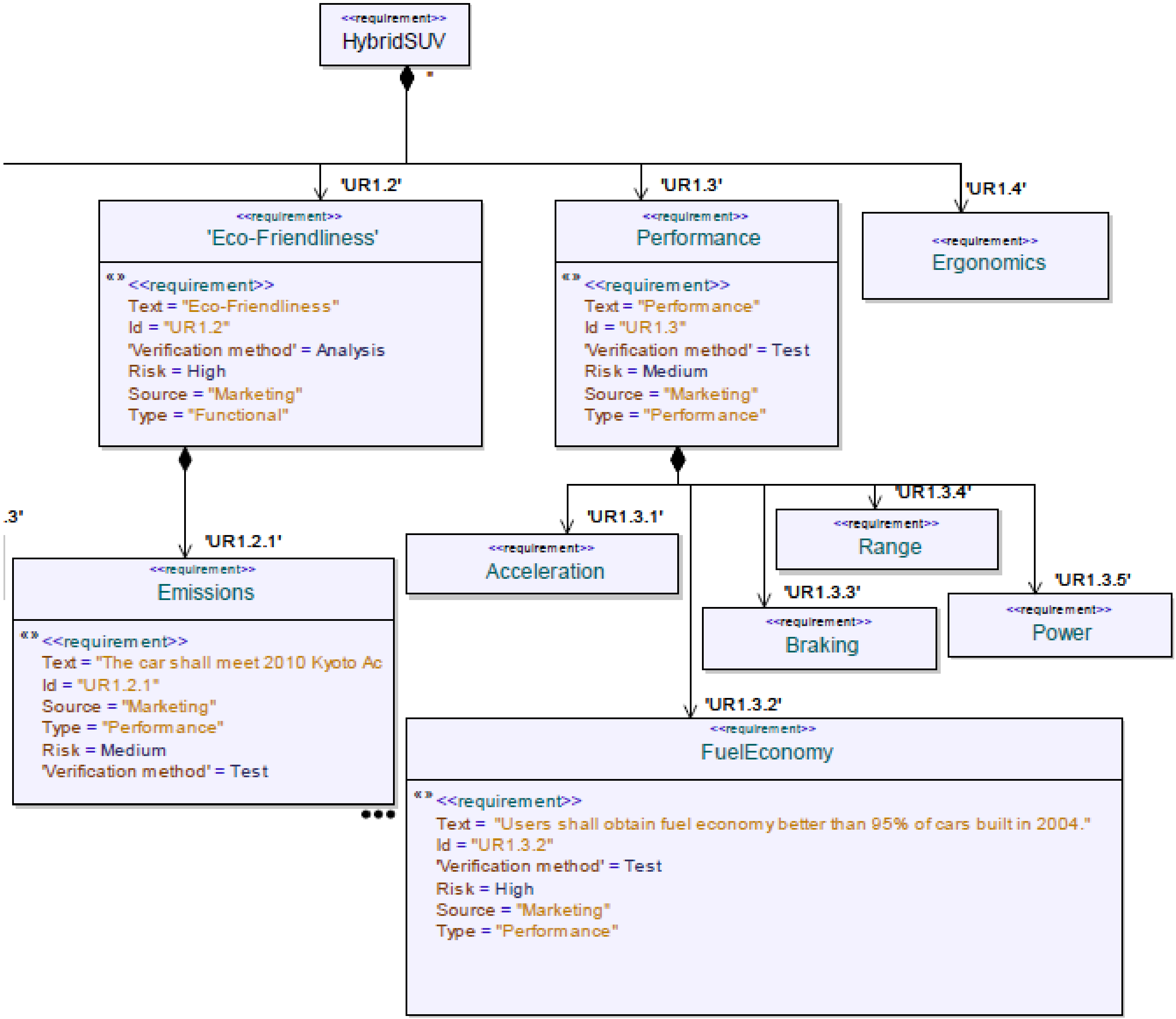
Modified from UML 2

New diagram type

Requirement diagram

- Requirements (textual) with identifier are model elements
  - <<requirement>> stereotype
  - Id (identifier) and text (description) fields
  - User-specified attributes: e.g., type, source, risk, ...
  - Tabular form is also supported
- Requirements can be grouped into hierarchic packages
  - Functional, performance, etc. categories
- Refinement among requirements (~ subclass), composition
- Relations can be used (e.g., inserted as structured comments):
  - Copy: between requirements (master – slave)
  - Trace: between requirements (client – supplier)
  - DeriveReqt: between requirements (source – derived)
  - Refine: between requirements and design elements
  - Satisfy: between requirements and design or implementation elements
  - Verify: between requirements and test elements

Example requirements diagram: Structure



<<requirement>>
Text = "Eco-Friendliness"
Id = "UR1.2"
'Verification method' = Analysis
Risk = High
Source = "Marketing"
Type = "Functional"

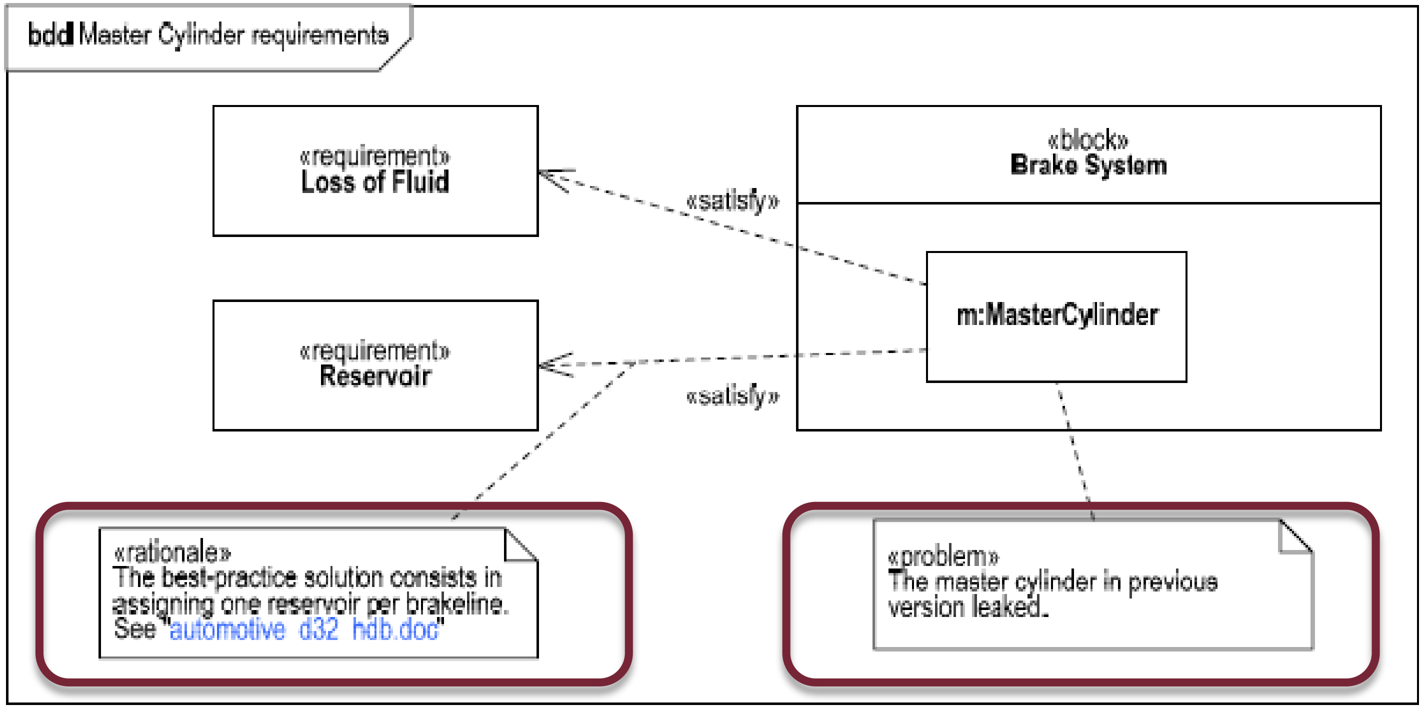
<<requirement>>
Text = "Performance"
Id = "UR1.3"
'Verification method' = Test
Risk = Medium
Source = "Marketing"
Type = "Performance"

<<requirement>>
Text = "The car shall meet 2010 Kyoto Ac
Id = "UR1.2.1"
Source = "Marketing"
Type = "Performance"
Risk = Medium
'Verification method' = Test

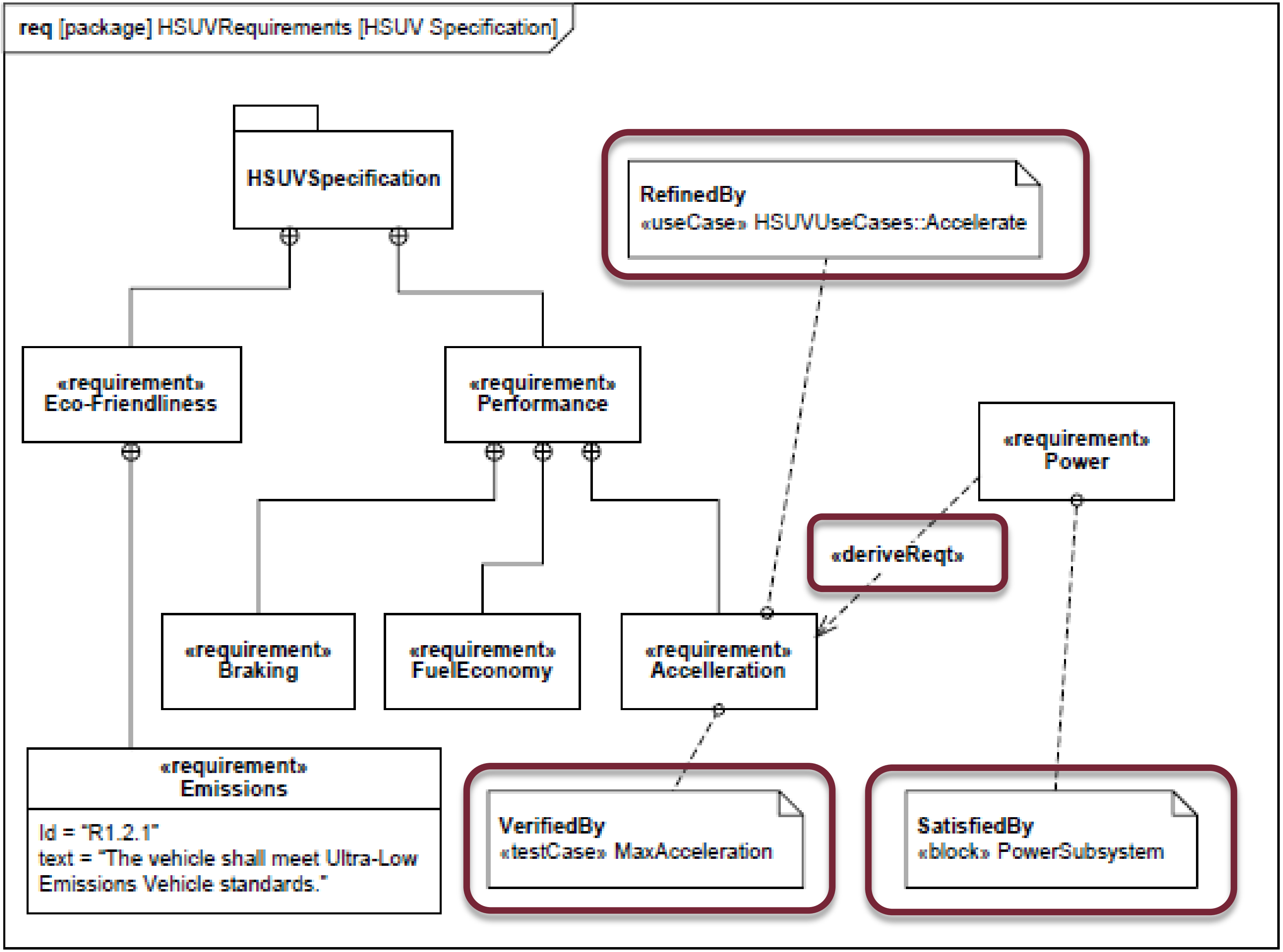
<<requirement>>
Text = "Users shall obtain fuel economy better than 95% of cars built in 2004."
Id = "UR1.3.2"
'Verification method' = Test
Risk = High
Source = "Marketing"
Type = "Performance"

Requirements diagram: Decisions

- Special comments (with predefined stereotype) can be assigned to any model element:
  - <<problem>>: Problem or proposal that needs decision
  - <<rationale>>: Rationale, solution, explanation



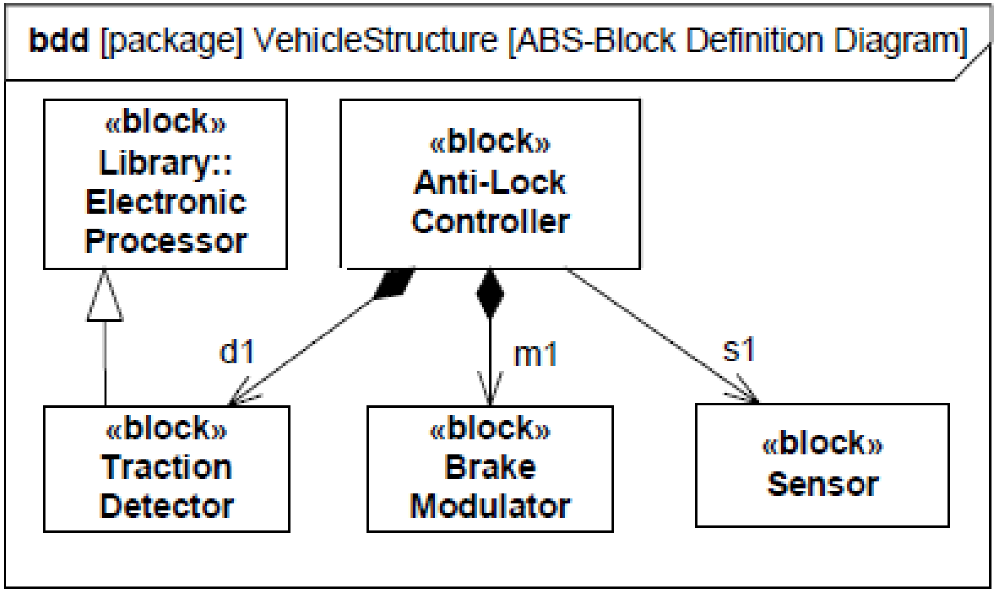
Example requirements diagram: Relations

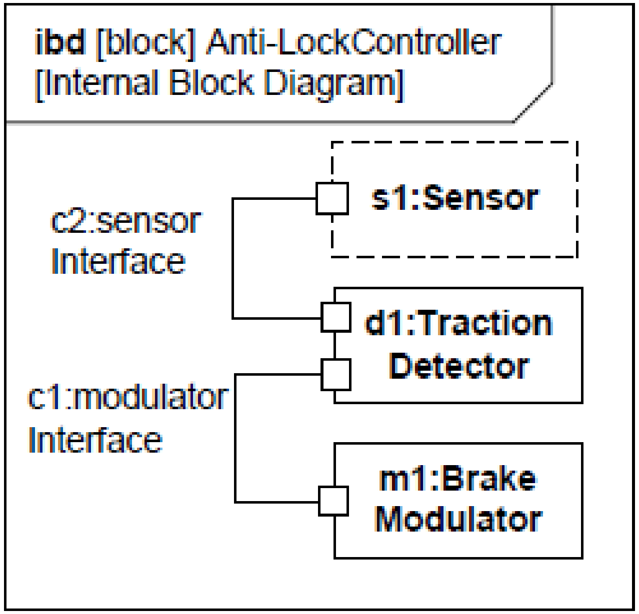


Id = "R1.2.1"
text = "The vehicle shall meet Ultra-Low Emissions Vehicle standards."

Block diagram

- Block: Element of the structure (black / white box)
  - Component (not only software)
  - In SysML: Based on UML 2.0 classes
- Block definition diagram: Types of blocks
- Internal block diagram: Concrete roles of block types





Parametric diagram

- Goal: Verifiable quantitative requirements (constraints) expressed using attributes
  - Non-functional requirements
  - Supporting analysis (e.g., performance, reliability)
- ConstraintBlock: Specifying interrelations
  - Formal (e.g., MathML, OCL), or informal (textual)
  - Adapted to analysis tool (not SysML specific)
- Parametric diagram: Concrete application
  - Application of Constraint blocks in a given context
  - Binding between values

Parametric diagram: Example

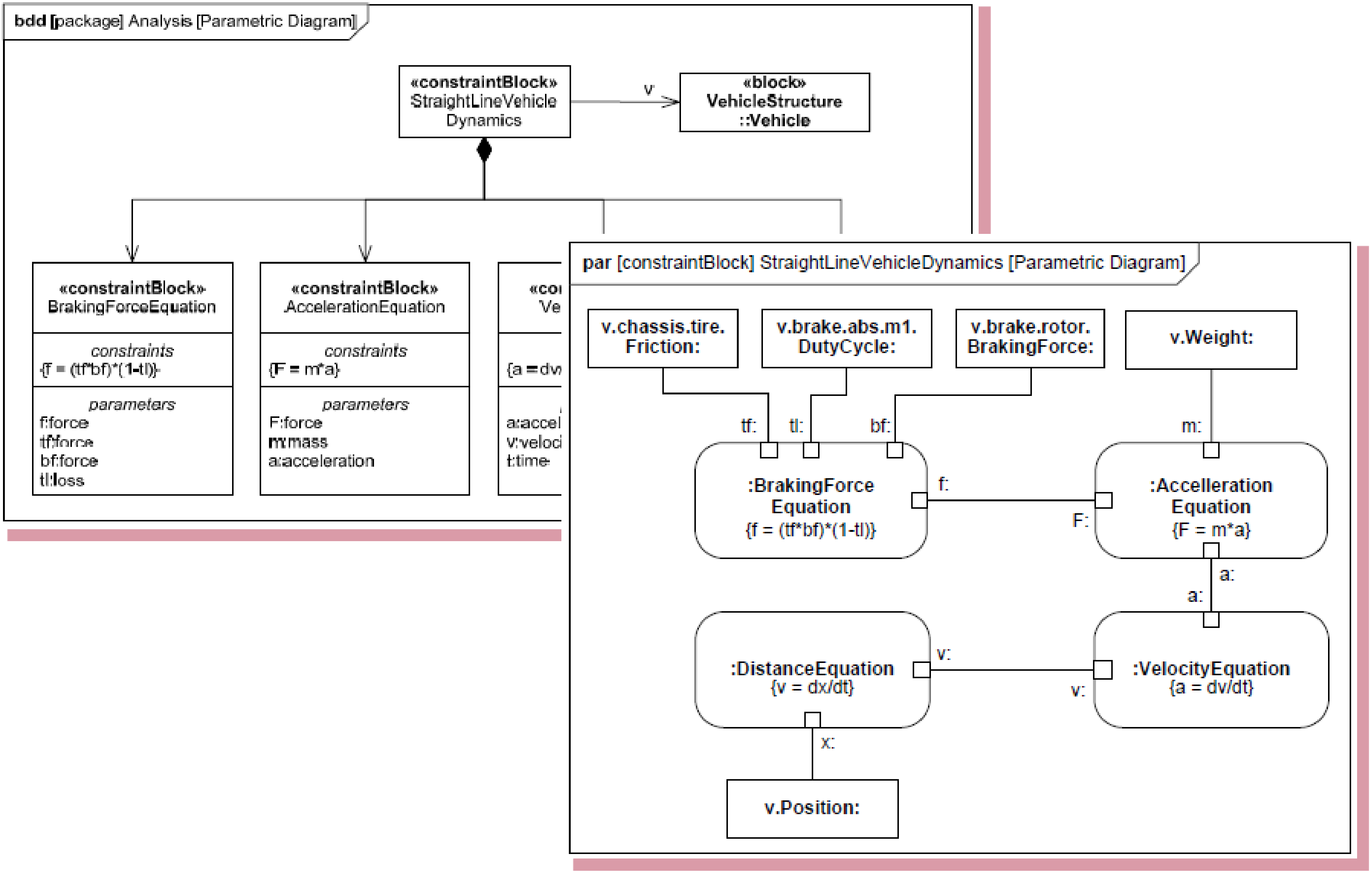
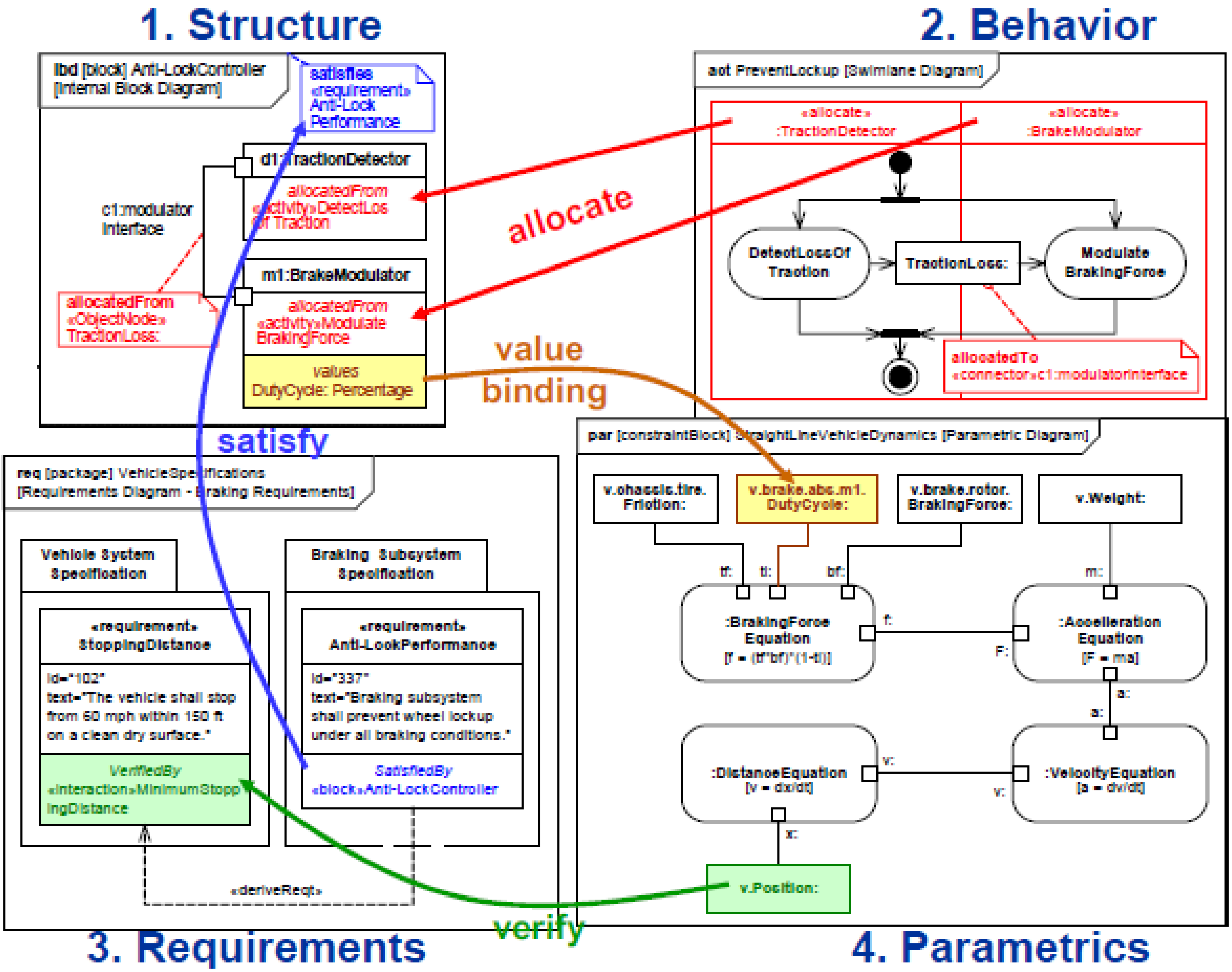


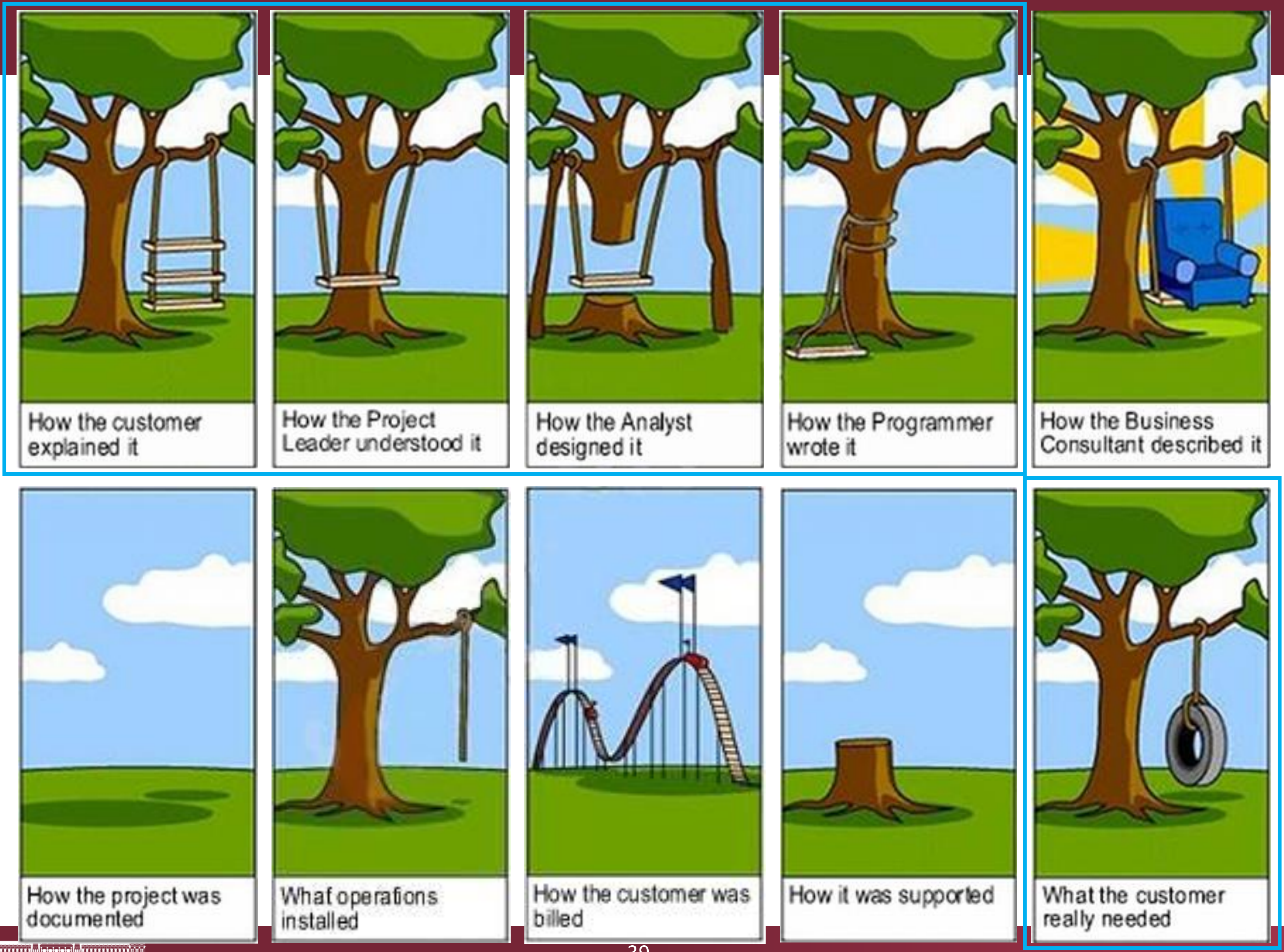
Illustration of the relations among diagrams



Verification tasks

General aspects and verification techniques

Verifying completeness and consistency


How the customer explained it
How the Project Leader understood it
How the Analyst designed it
How the Programmer wrote it
How the Business Consultant described it
How the project was documented
What operations installed
How the customer was billed
How it was supported
What the customer really needed

General criteria for a good specification

- Complete
  - Specified functions, references, tools, …
- Consistent
  - Internal and external consistency
  - Traceability
- Verifiable
  - Specific
  - Unambiguous
  - Quantifiable (if possible)
- Feasible
  - Resources
  - Usability
  - Maintainability
  - Risks: budget, technical, environmental

Example: Good specification on the basis of IEEE 830-1998

Correct

- Every requirement stated therein is one that the software shall meet
- Consistent with external sources (e.g. standards)

Unambiguous

- Every requirement has only one interpretation
- Formal or semi-formal specification languages can help

Complete

- For every (valid, invalid) input there is specified behavior
- TBD only possible resolution

Consistent

- No internal contradiction, well-defined terminology

Ranked for importance and/or stability

- Necessity of requirements

Verifiable

- Can be checked whether the requirement is met

Modifiable

- Not redundant, structured

Traceable

- Source is clear, effect can be referenced

Example: Good specification on the basis of IEEE 29148-2011

Necessary

- If it is removed or deleted, a deficiency will exist, which cannot be fulfilled by other capabilities

Implementation-free

- Avoids placing unnecessary constraints on the design

Unambiguous

- It can be interpreted in only one way; is simple and easy to understand

Consistent

- Is free of conflicts with other requirements

Complete

- Needs no further amplification (measurable and sufficiently describes the capability)

Singular

- Includes only one requirement with no use of conjunctions

Feasible

- Technically achievable, fits within system constraints (cost, schedule, regulatory…)

Traceable

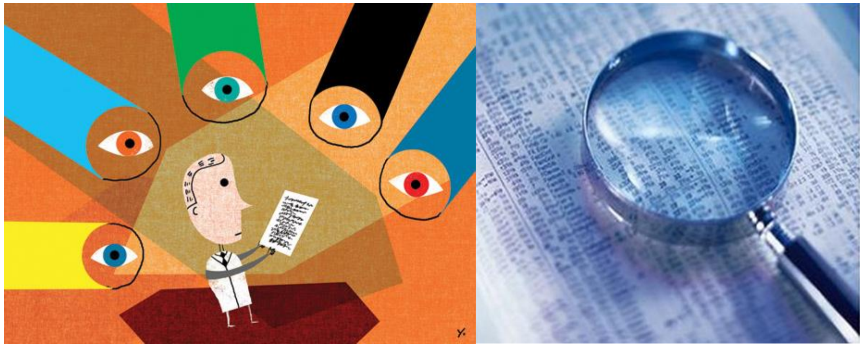
- Upwards traceable to the stakeholder statements; downwards traceable to other documents

Verifiable

- Has the means to prove that the system satisfies the specified requirement

Techniques for verification

- Static analysis
  - Checking documents, code or other artifacts
  - Without execution
- Basis for static analysis: Checklists
  - Examples: Criteria for good specification
  - Completeness of the checklist is always questionable
- Implementation of static analysis
  - Manual review (all aspects)
  - Tool-support (esp. for checking consistency)



Manual review: Terminology and steps

Types of review:

- Informal review
    - No formal process
    - Peer or technical lead reviewing
- Walkthrough
    - Meeting led by author
    - May be quite informal
- Technical review
    - Review meeting with experts
    - Pre-meeting preparations for reviewers
- Inspection
    - Formal (well-documented) process
    - Led by a trained moderator

Steps of a review:

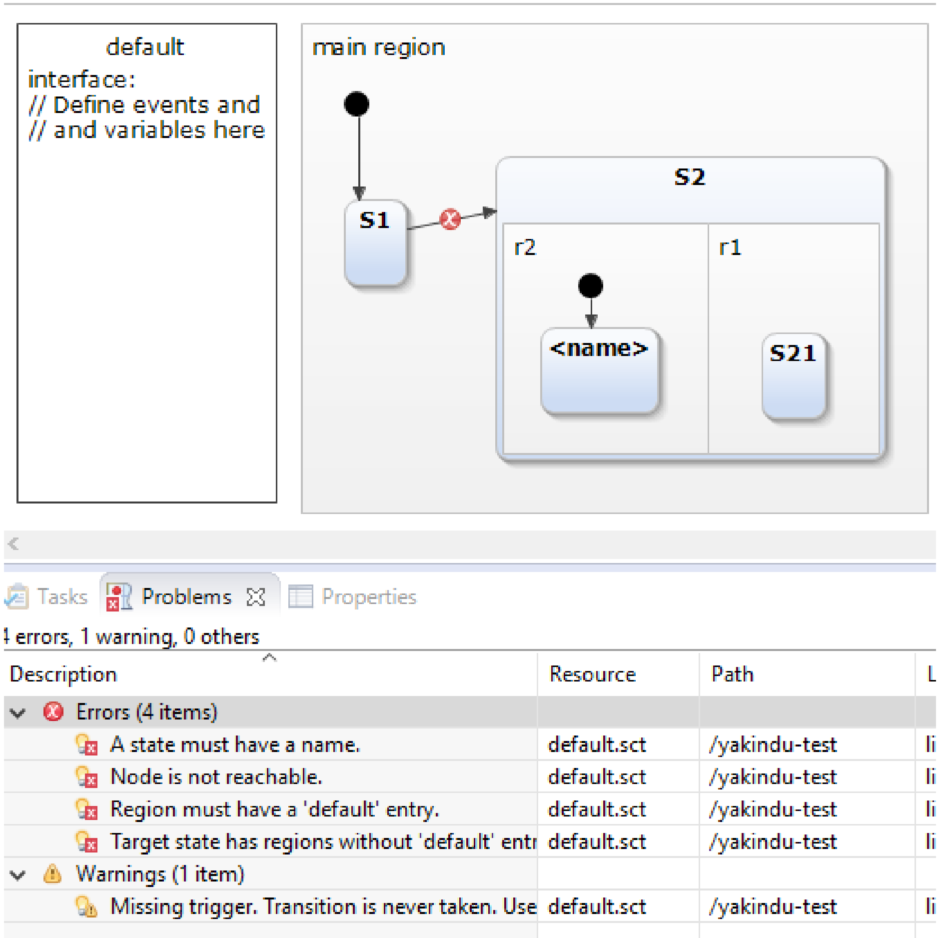
1. Planning
    - Defining review criteria
    - Allocating roles
2. Kick-off
    - Distributing documents
    - Explaining objectives
3. Individual preparation
    - Reviewing artifacts
    - Collecting defects, questions
4. Review meeting
    - Discussing and logging results
    - Making decisions
5. Rework
    - Fixing defects
    - Recording updated status
6. Follow-up
    - Checking fixes
    - Checking exit criteria

Tool support for verification of the specification

- Natural languages
  - Static analysis by manual review
- Semi-formal languages
  - Precise syntax, but informal semantics
  - Automated checking of syntax and well-formedness (missing or contradictory elements)
- Formal languages
  - Mathematically precise syntax and semantics
  - Automated checking of syntax / well-formedness
  - Automated checking of behavior
    - Operational semantics: Reachable states of computation (e.g., model checking, equivalence/refinement checking)
    - Axiomatic semantics: Properties of computation (e.g., theorem proving for invariants, post-conditions)

Tool support: Checking state machines

Yakindu Statechart Tools

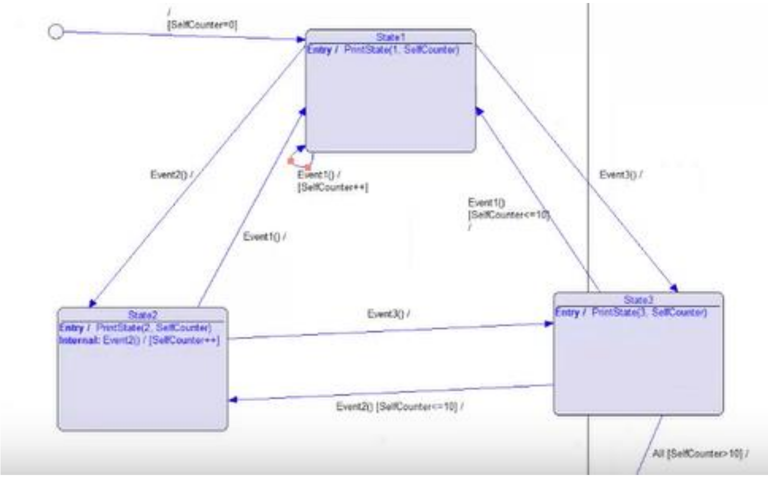


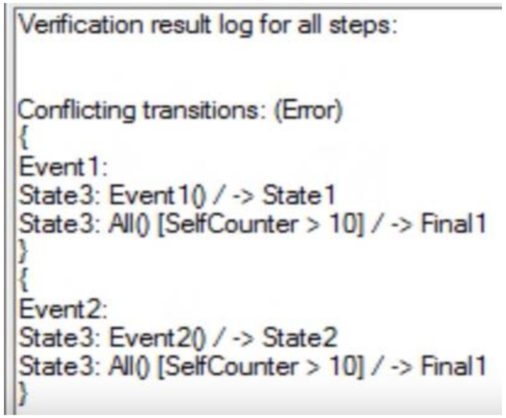
4 errors, 1 warning, 0 others

Description	Resource	Path	L
Errors (4 items)			
A state must have a name.	default.sct	/yakindu-test	li
Node is not reachable.	default.sct	/yakindu-test	li
Region must have a 'default' entry.	default.sct	/yakindu-test	li
Target state has regions without 'default' entr	default.sct	/yakindu-test	li
Warnings (1 item)			
Missing trigger. Transition is never taken. Use	default.sct	/yakindu-test	li

https://www.youtube.com/watch?v=uO6MASCBPrg

IAR visualSTATE





```
Verification result log for all steps:


Conflicting transitions: (Error)
{
Event1:
State3: Event1() / -> State1
State3: All() [SelfCounter > 10] / -> Final1
}
{
Event2:
State3: Event2() / -> State2
State3: All() [SelfCounter > 10] / -> Final1
}
```

https://www.youtube.com/watch?v=05lTlymLugM

Verifying completeness and consistency

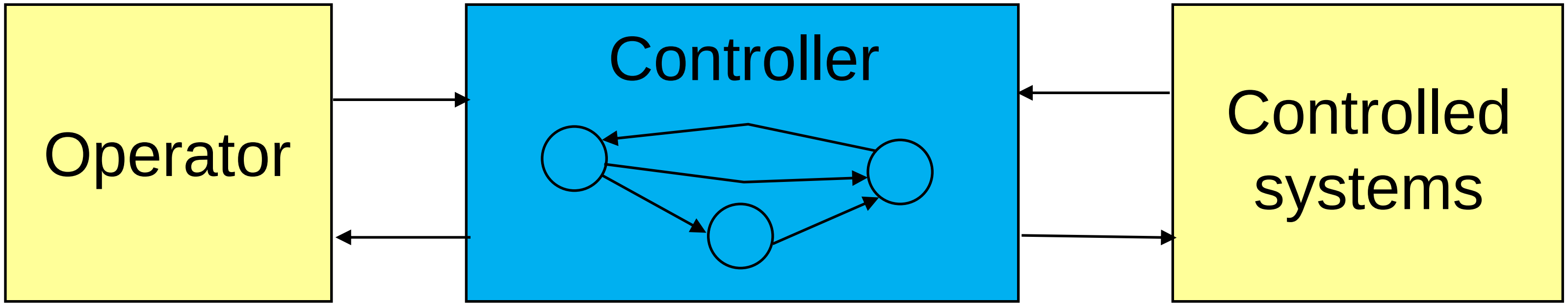
Incompleteness or inconsistency: major source of failures

- Statistics of faults found during the system testing of Voyager and Galileo spacecraft:
  78% (149/192) faults resulting from specification problem
  - 23%: missing state transitions (stuck in dangerous state)
  - 16%: missing time constraints for data validity
  - 12%: missing reaction to external event
  - 10%: missing assertions to check input values
- 60-70% of IT project failures can be traced back to insufficient requirements – Meta Group (2003)
- “Significantly more defects were found per page at the earlier phases of the software life cycle.”
  - Inspection of 203 documents
  - An analysis of defect densities found during software inspections (JSS, DOI: 10.1016/0164-1212(92)90089-3)

Example: Review criteria for reactive systems

Groups of criteria (developed by N. Leveson, Safeware)

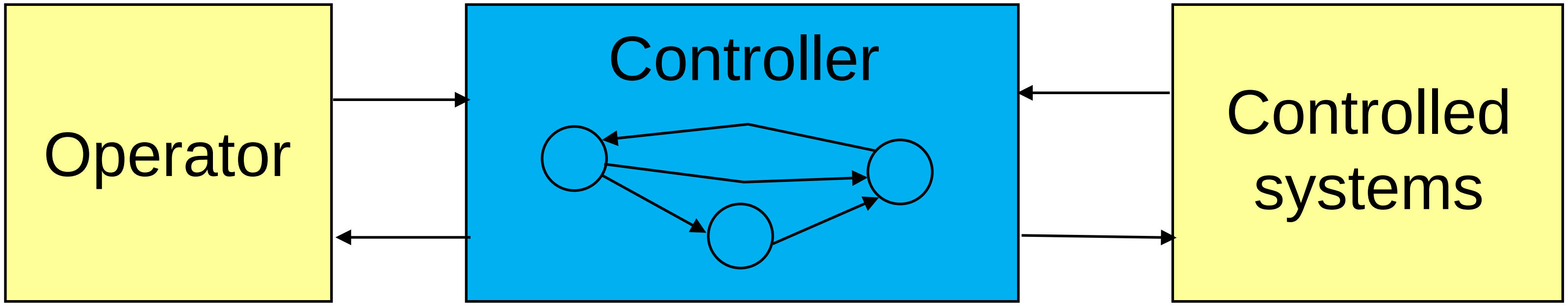
- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interface



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interface

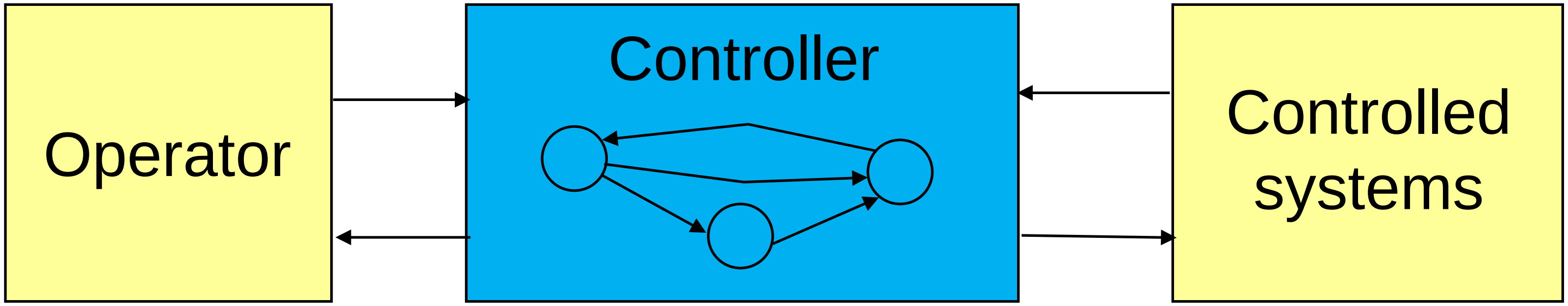
- Initial state is safe
- In case of missing input there is a timeout, and no action is allowed



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interfac

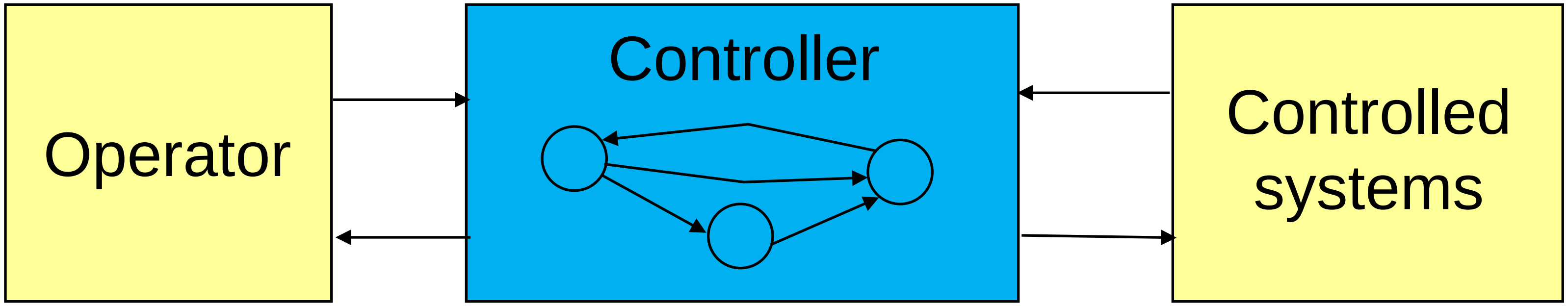
- For every input in every state there is a specified behavior
- Reactions are unambiguous (deterministic)
- Input is checked (value, timeliness)
- Handling of invalid inputs is specified
- Rate of interrupts is limited



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interface

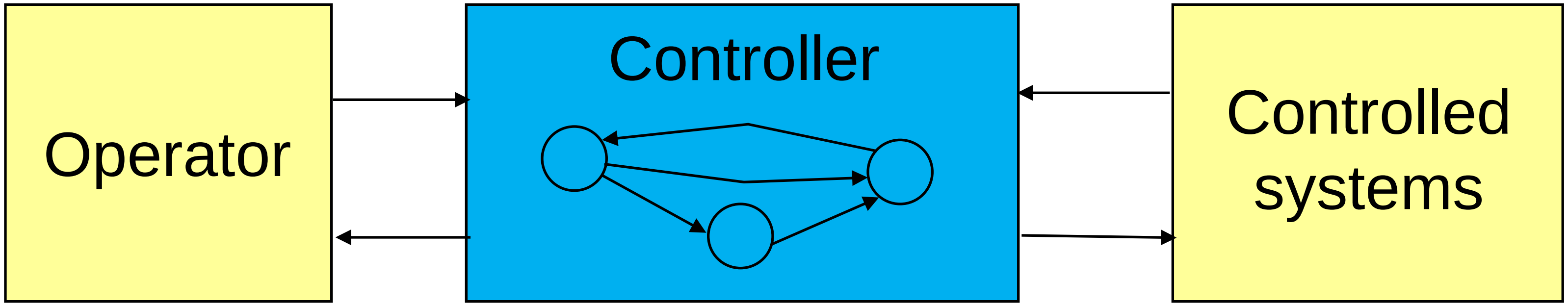
- Credibility checks are specified
- There is no unused output
- Processing capability of the environment is respected



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interface

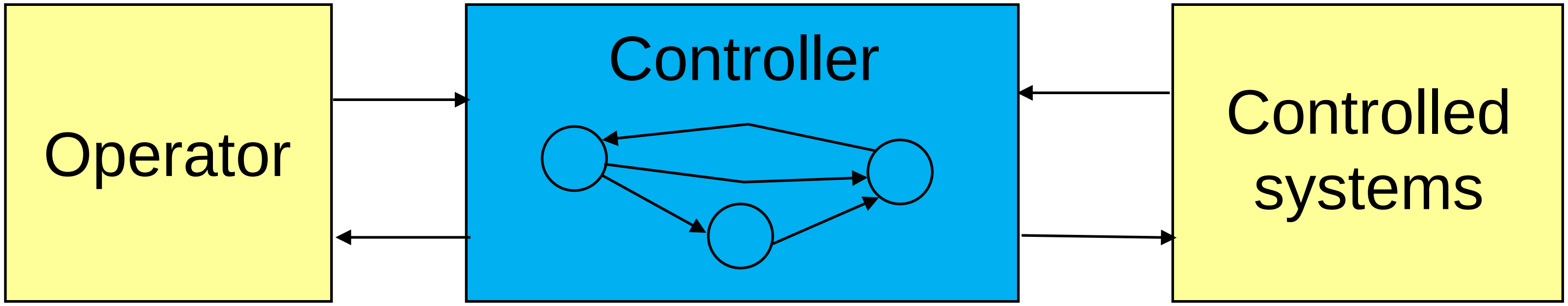
- Effect of outputs is checked through the inputs
- Control loop is stable



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interfa

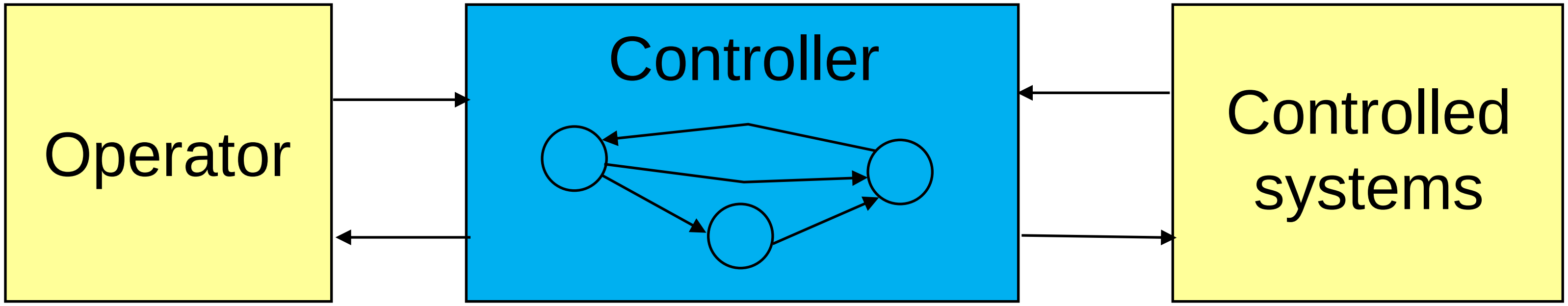
- Every state is reachable statically (incoming path)
- Transitions are reversible (there is a way back)
- More than one transitions from dangerous to safe states
- Confirmed transitions from safe to dangerous states



Example: Review criteria for reactive systems

- State definition
- Inputs (events)
- Outputs
- Outputs and triggers
- Transitions
- Human-machine interface

- Priority of events to the operator is defined
- Update rate is defined
- Processing capability of the operator is respected



Managing requirements

Traceability

Basic tasks and tool support

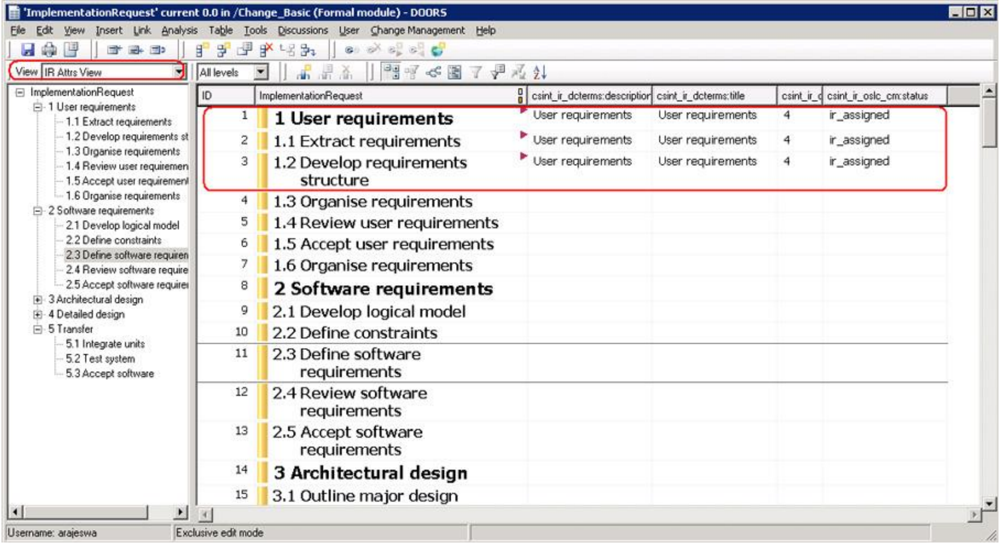
The role of traceability

- Traceability of requirements: Managing links among requirements and design artifacts
  - Among various levels of requirements: User –> System –> Module
  - Among requirements and design artifacts:
    Req. specification –> Architecture design –> Module design –> Source code –> Test –> Test result
- Analysis possibilities based on traceability links
  - Impact analysis: handling the changes
    - What is affected by a changed requirement?
  - Derivation analysis: handling utility and rewards
    - Why is this artifact here? What is the related requirement?
  - Coverage analysis: handling the status of development
    - What requirements are refined / implemented / tested?

Typical tasks of requirement management tools

Storing the requirements:	Hierarchic grouping
Handling the lifecycle and changes of requirements:	Using versions, attributes, timestamps, showing timeline of changes
Storing the relations:	Several types: Composition, derivation, refinement, implementation, ..
Support traceability:	Requirements – Design (models) – Source code – Test – Test results
Navigation on relations:	Forward: e.g., impact analysis Backward: e.g., derivation analysis
Generation of coverage lists:	Identify uncovered requirements or extra functionality
Handling authorization:	Defining roles and allowed activities
Sending notifications:	Messages in case of changes
Assuring integrity:	Detecting unintentional changes

Requirement management tools



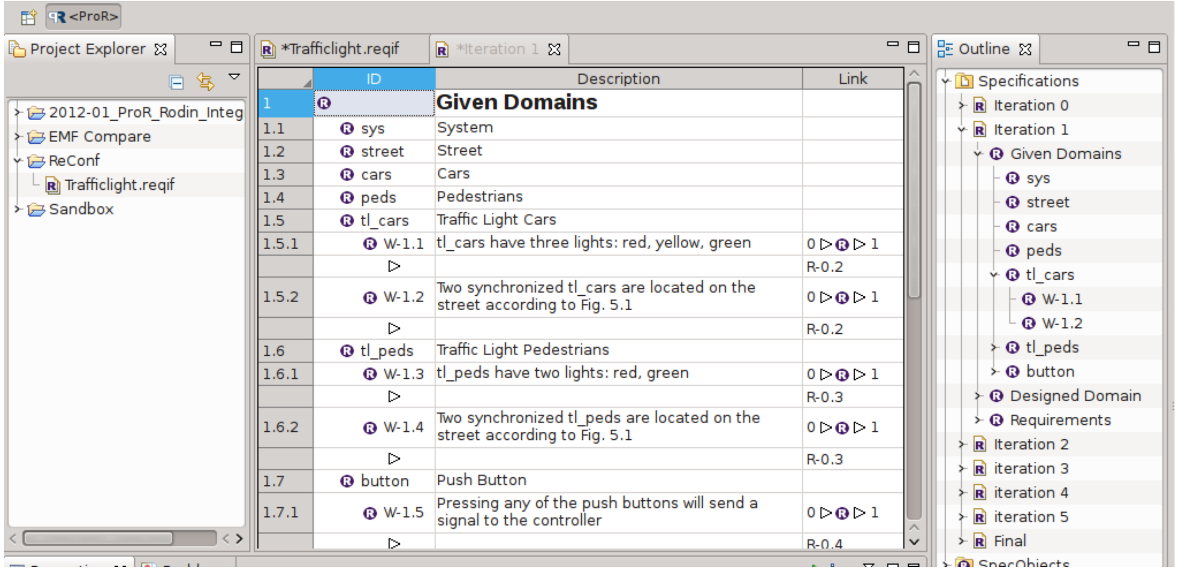
ID	ImplementationRequest	csint_ir_dcterms:description	csint_ir_dcterms:title	csint_ir_c	csint_ir_oslc_cm:status
1	**1 User requirements**	User requirements	User requirements	4	ir_assigned
2	1.1 Extract requirements	User requirements	User requirements	4	ir_assigned
3	1.2 Develop requirements structure	User requirements	User requirements	4	ir_assigned
4	1.3 Organise requirements				
5	1.4 Review user requirements				
6	1.5 Accept user requirements				
7	1.6 Organise requirements				
8	**2 Software requirements**				
9	2.1 Develop logical model				
10	2.2 Define constraints				
11	2.3 Define software requirements				
12	2.4 Review software requirements				
13	2.5 Accept software requirements				
14	**3 Architectural design**				
15	3.1 Outline major design				

IBM Rational DOORS Next Generation

https://www.youtube.com/watch?v=qYK7_g4Fy44

ProR

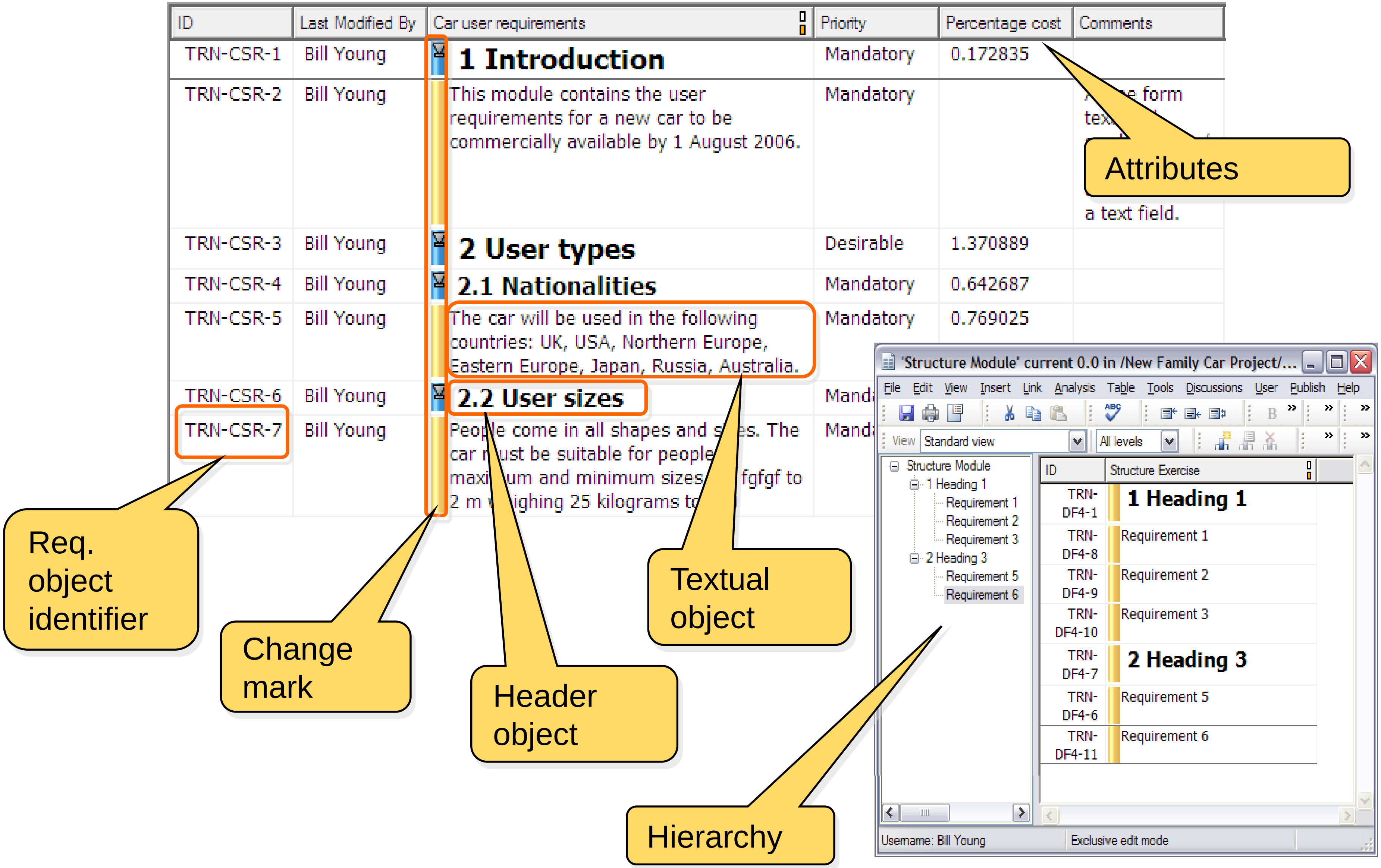
ReqIF Requirements Interchange Format



	ID	Description	Link
1		**Given Domains**	
1.1	sys	System	
1.2	street	Street	
1.3	cars	Cars	
1.4	peds	Pedestrians	
1.5	tl_cars	Traffic Light Cars	
1.5.1	W-1.1	tl_cars have three lights: red, yellow, green	0 ▷ Ⓡ ▷ 1
	▷		R-0.2
1.5.2	W-1.2	Two synchronized tl_cars are located on the street according to Fig. 5.1	0 ▷ Ⓡ ▷ 1
	▷		R-0.2
1.6	tl_peds	Traffic Light Pedestrians	
1.6.1	W-1.3	tl_peds have two lights: red, green	0 ▷ Ⓡ ▷ 1
	▷		R-0.3
1.6.2	W-1.4	Two synchronized tl_peds are located on the street according to Fig. 5.1	0 ▷ Ⓡ ▷ 1
	▷		R-0.3
1.7	button	Push Button	
1.7.1	W-1.5	Pressing any of the push buttons will send a signal to the controller	0 ▷ Ⓡ ▷ 1
	▷		R-0.4

https://www.youtube.com/watch?v=YC_NrseqWcc

Example: IBM Rational DOORS



ID	Last Modified By	Car user requirements	Priority	Percentage cost	Comments
TRN-CSR-1	Bill Young	**1 Introduction**	Mandatory	0.172835	
TRN-CSR-2	Bill Young	This module contains the user requirements for a new car to be commercially available by 1 August 2006.	Mandatory		e form tex a text field.
TRN-CSR-3	Bill Young	**2 User types**	Desirable	1.370889	
TRN-CSR-4	Bill Young	**2.1 Nationalities**	Mandatory	0.642687	
TRN-CSR-5	Bill Young	The car will be used in the following countries: UK, USA, Northern Europe, Eastern Europe, Japan, Russia, Australia.	Mandatory	0.769025	
TRN-CSR-6	Bill Young	**2.2 User sizes**	Mand		
TRN-CSR-7	Bill Young	Peop le come in all shapes and s es. The car ust be suitable for people maxi um and minimum sizes fgfgf to 2 m ighing 25 kilograms to	Mand		

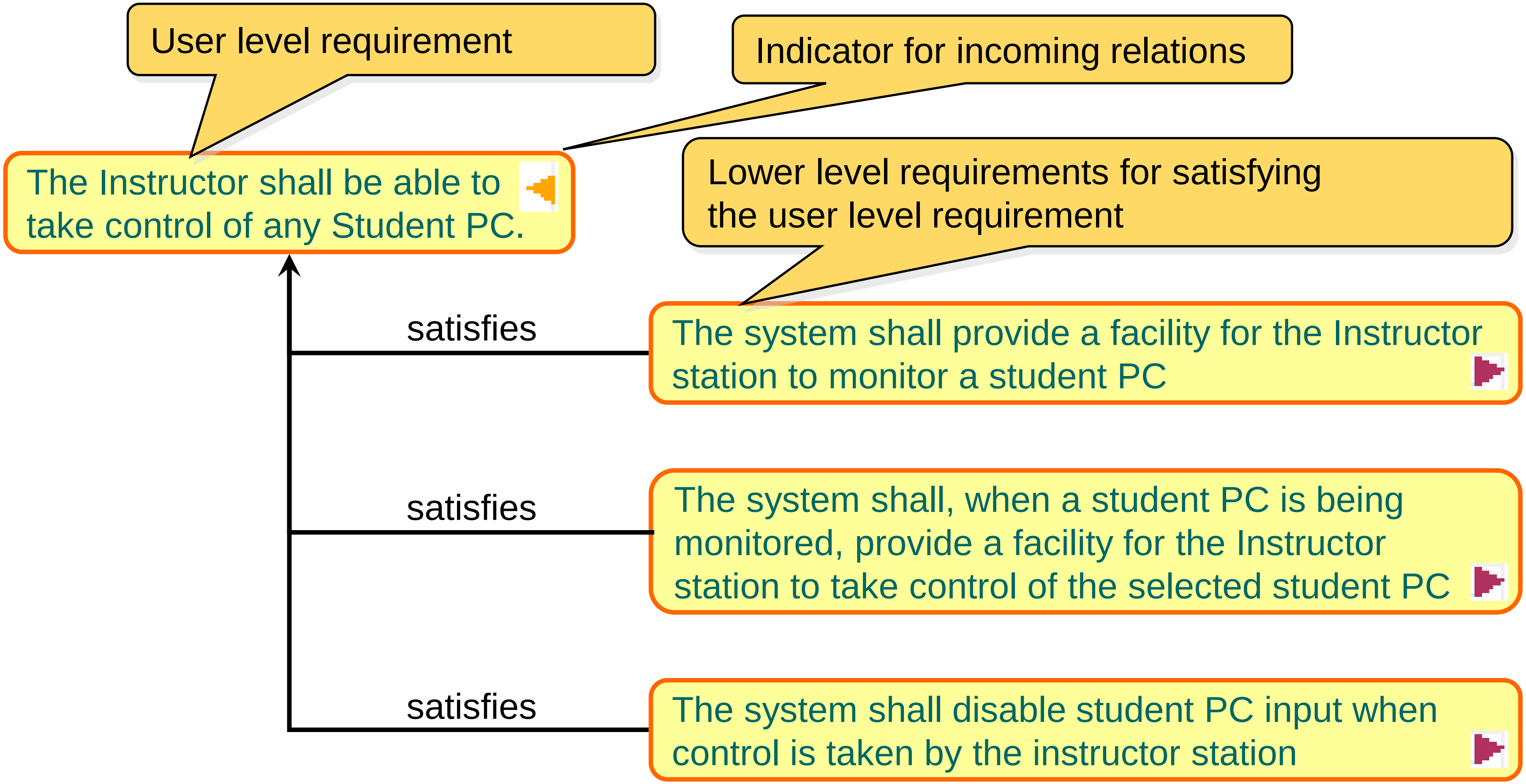
'Structure Module' current 0.0 in /New Family Car Project/...

- Structure Module
  - 1 Heading 1
    - Requirement 1
    - Requirement 2
    - Requirement 3
  - 2 Heading 3
    - Requirement 5
    - Requirement 6

ID	Structure Exercise
TRN-DF4-1	**1 Heading 1**
TRN-DF4-8	Requirement 1
TRN-DF4-9	Requirement 2
TRN-DF4-10	Requirement 3
TRN-DF4-7	**2 Heading 3**
TRN-DF4-6	Requirement 5
TRN-DF4-11	Requirement 6

Username: Bill Young
Exclusive edit mode

Example: IBM Rational DOORS



User level requirement

Indicator for incoming relations

The Instructor shall be able to take control of any Student PC.

Lower level requirements for satisfying the user level requirement

satisfies

The system shall provide a facility for the Instructor station to monitor a student PC

satisfies

The system shall, when a student PC is being monitored, provide a facility for the Instructor station to take control of the selected student PC

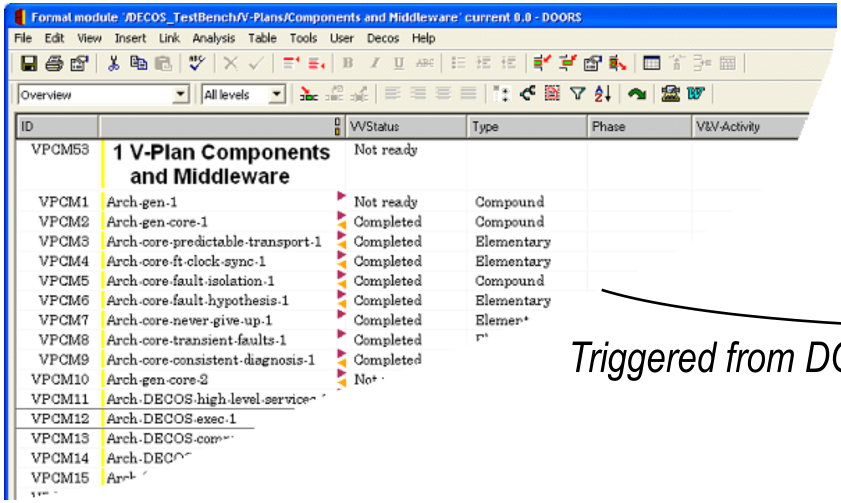
satisfies

The system shall disable student PC input when control is taken by the instructor station

Requirement based verification tool-chains

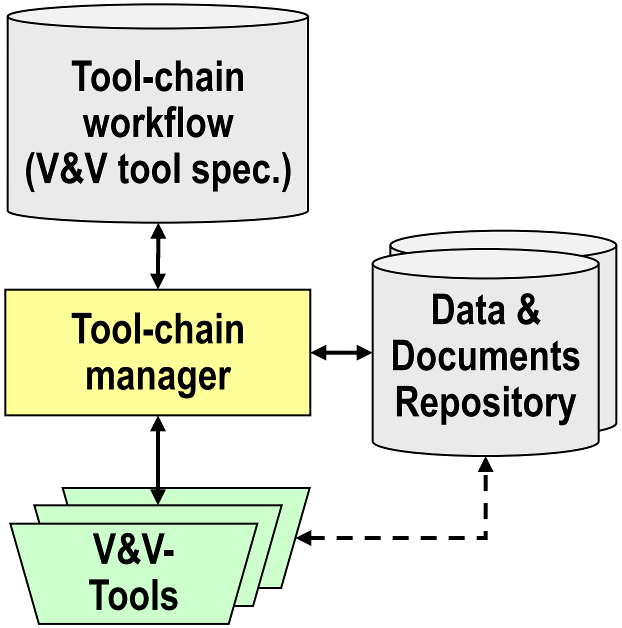
- Assigning verification activities to requirements
  - Checking satisfaction of the req., collecting evidences
  - Standard-based techniques and measures (e.g., for safety case)
- Verification tool-chains (typically external)
  - Analysis: Generating analysis model, performing analysis, post-processing or visualization of results
  - Testing: (Model based) test case generation, test execution, providing test verdict
  - Measuring: Configuring measurements, executing measurements, data analysis
- Verification tool-chains can be started from the requirement management tool
  - Scripts with triggers (verifiable requirement)
- Registering the status of verification
  - Successfully verified requirement + repository of evidences

Example: Starting verification tool chain from DOORS



Formal module '/DECOS_TestBench/V-Plans/Components and Middleware' current 0.0 - DOORS

ID		VVStatus	Type	Phase	V&V-Activity
VPCM53	**1 V-Plan Components and Middleware**	Not ready			
VPCM1	Arch-gen-1	Not ready	Compound		
VPCM2	Arch-gen-core-1	Completed	Compound		
VPCM3	Arch-core-predictable-transport-1	Completed	Elementary		
VPCM4	Arch-core-ft-clock-sync-1	Completed	Elementary		
VPCM5	Arch-core-fault-isolation-1	Completed	Compound		
VPCM6	Arch-core-fault-hypothesis-1	Completed	Elementary		
VPCM7	Arch-core-never-give-up-1	Completed	Elemen		
VPCM8	Arch-core-transient-faults-1	Completed			
VPCM9	Arch-core-consistent-diagnosis-1	Completed			
VPCM10	Arch-gen-core-2	Not			
VPCM11	Arch-DECOS-high-level-service				
VPCM12	Arch-DECOS-exec-1				
VPCM13	Arch-DECOS-com				
VPCM14	Arch-DEC				
VPCM15	Arc				



Example tools:

- ITEM (Hazard and risk analysis)
- RACER (Formal verification)
- SCADE MTC (Simulation)
- LDRA (Testing)
- PROPANE (Fault injection)
- EMI Test Bench

Summary

- Inputs and outputs of the phase
- Preparing the requirements specification
  - Formal languages
  - Semi-formal and structured methods
- Verification tasks
  - General aspects and verification techniques
  - Verifying completeness and consistency
- Managing requirements
  - Traceability
  - Basic tasks and tool support